

# **Computer Architecture MCQs**

By: Prof. Fazal Rehman Shamil Last Modified: August 13, 2020

Next Page	Previous Page	
-----------	---------------	--

### Computer Architecture MCQs

#### 1. Which format is used to store data?

- A. Hexadecimal
- B. Octal
- C. Decimal
- D. BCD
  - ☐ Answer Click Here:

D

## 2. A computer add and compare data at \_\_\_\_\_?

- A. CPU chip
- B. Memory chip
- C. Hard disk
- D. Floppy disk
- ☐ Answer Click Here:

Α

#### is the 8-bit encoding format used to store data in a computer:

- A. ANCI
- B. USCII
- C. ASCII
- D. EBCDIC
- ☐ Answer Click Here:

D

#### 4. What does a microcomputer system consist of?

- A. Memory
- B. Peripheral equipment
- C. Microprocessor
- D. All of these
- ☐ Answer Click Here:

D

#### 5. \_\_\_\_\_ is used in a source program:

- A. Natural language
- B. Assembly language
- C. High-level language
- D. Machine level language
- ☐ Answer Click Here:

Search

# **MCQS**

MCQs - Database Systems

MCQs - Computer Network

MCQs Data Structures

MCQs-Computer Science Basics

MCQs - Computer Science

MCQs - English

MCQs - Biology

MCQs - Everyday Science

MCQs – General Knowledge

MCQs - Islamic studies

MCQs - Maths

MCQs - Physics

MCQs - Geography

MCQs - Economics

MCQs - Statistics

MCQs - Programming C Plus

Plus

MCQs - Ethics

MCQs - Visual Programming

MCQs - Management Sciences

MCQs - Social Studies

MCQs – Communication skills

MCQs - General

Engineering MCQs Homepage

Psychology MCQs

Philosophy Of Science

6. Which operation a CPU do not perform?
A. <u>Arithmetic operation</u>
B. Data transfer
C. Logic operation
D. All of these
☐ Answer - Click Here:
D
7 is made of semiconductors:
A. Hard-disk
B. RAM
C. CD disk
D. Floppy disk
Answer - Click Here:
В
8. What is a pipelining strategy called?
A. Instruction manipulation
B. Instruction decoding
c. Instruction prefetch
D. Instruction execution
☐ Answer - Click Here:
C
9. Fast & Small RAMs are known as:
A. Cache
B. Stacks
C. Heaps
D. Accumulators
☐ Answer - Click Here:
A
10. What is a stack?
A. 16-bit register in the microprocessor
B. 8-bit register in the microprocessor
C. 16-bit memory address stored in the program counter
D. set of memory locations in R/WM reserved for storing information temporarily during
the execution of computer
☐ Answer - Click Here:
D
11. To store the intermediate results the ALU takes help from:
A. Registers
B. Accumulators
C. Stack
D. Heap
<b>-</b>
☐ Answer - Click Here:
В

- A. The first memory location where a subroutine address is stored  $% \left( 1\right) =\left( 1\right) \left( 1\right) +\left( 1\right) \left( 1\right) \left( 1\right) +\left( 1\right) \left( 1\right)$
- B. A register in which flag bits are stored
- C. 16-bit register in the microprocessor that indicates the beginning of the stack memory
- D. A register that decodes and executes the 16-bit arithmetic expression

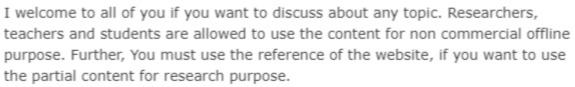
☐ Answer - Click Here:

C



### Prof. Fazal Rehman Shamil

CEO @ T4Tutorials.com



T4Tutorials.com Copyright © 2020.

All Copy Rights Reserved By T4Tutorials.com Back to Top ↑