Computer Architecture Multiple Choice Questions

By: Prof. Fazal Rehman Shamil Last Modified: August 13, 2020

Computer Architecture Multiple Choice Questions

1. What does the control unit generate to control other units?

- A. Transfer signals
- B. Command Signals
- C. Control signals
- D. Timing signals

Answer -	Click	Here:
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D

2. _____ is the branch logic that provides decision-making capabilities in the control unit:

- A. Unconditional transfer
- B. Controlled transfer
- C. Conditional transfer
- D. None of these
- ☐ Answer Click Here:

Α

3. What are encoded characters & numbers used as operands?

- A. Stored Values
- B. Information
- C. Data
- D. Input

☐ Answer - Click Here:

C

4. Any instruction initiates interrupt which are:

- A. hardware
- B. software
- C. internal
- D. external

☐ Answer - Click Here:

В

5. When does the input devices send information to the processor?

- A. When the data arrives regardless of the SIN flag
- B. When the SIN status flag is set
- C. Either of the cases
- D. Neither of the cases

☐ Answer - Click Here:

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6. What do processors of all computers must have?
A. Control unit
B. ALU
C. Primary Storage
D. All of these
☐ Answer - Click Here:
D
7. Which of the following bus structure is used to connect I/O devices?
A. Star bus
B. Rambus
C. Single bus
D. Multiple bus
☐ Answer - Click Here:
С
8. In the CPU, what is the functionality of the control unit?
A. To decode program instruction
B. To perform logic operations
C. To store program instruction
D. To transfer data to primary storage
□ Answer - Click Here:
□ Answer - Click Here:
A
9 With the help of the memory access times
9. With the help of we reduce the memory access time:
A. SDRAM
A. SDRAM B. Cache
A. SDRAM B. Cache C. Heaps
A. SDRAM B. Cache
A. SDRAM B. Cache C. Heaps
A. SDRAM B. Cache C. Heaps D. Higher capacity RAMs
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12. The most common addressing techniques employed by a CPU is:

A. Direct
B. Indirect
C. Immediate
D. All of these
Angway Click Horos

☐ Answer - Click Her

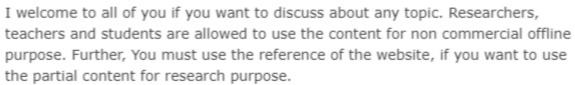
D





Prof. Fazal Rehman Shamil

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