Important MCQs of DLD

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1. Why we use demultiplexer?
A. Route the data from a single input to one of many outputs  
B. Select data from several inputs and route it to a single output  
C. Perform serial to parallel conversion  
D. Both a and b

[Answer - Click Here]

D

2. Which is an example of synchronous inputs?
A. Preset Input (PRE)  
B. EN Input  
C. J-K input  
D. Clear Input (CLR)

[Answer - Click Here]

C

3. Which one is the Second step of making transition table?
A. determining feedback loop  
B. designating output of loops  
C. deriving functions of Y  
D. plotting

[Answer - Click Here]

B

4. We can be imagined that an or gate is look like
A. Switches connected in parallel  
B. Switches connected in series  
C. MOS transistors connected in series  
D. None of these

[Answer - Click Here]

A

5. The change from a current state to the next state is determined by
A. Previous state and outputs  
B. Current state and outputs  
C. Current state and the inputs  
D. Previous state and inputs

[Answer - Click Here]
6. Each gate take time for delay 
   A. 2 to 10 ns  
   B. 3 to 10 ns  
   C. 1 to 5 ns  
   D. 3 to 5 ns  

   □ Answer - Click Here:  
   A

7. In Which combination of gates the arbitrary Boolean function is not possible?
   A. OR gates and exclusive OR gate only  
   B. NAND gates only  
   C. OR gates and NOT gates only  
   D. OR gates and AND gates only  

   □ Answer - Click Here:  
   D

8. Which one of the following is used to simplify the circuit that determines the next state?
   A. State diagram  
   B. State assignment  
   C. State reduction  
   D. Next state table  

   □ Answer - Click Here:  
   A

9. When both inputs are ___________ then NAND latch works.
   A. inverted  
   B. 0  
   C. 1  
   D. don’t cares  

   □ Answer - Click Here:  
   C

10. ___________adders are needed to construct an m-bit parallel adder.
    A. m+1  
    B. m-1  
    C. m  
    D. m/2  

    □ Answer - Click Here:  
    B

11. ___________ is converted by a multiplexer with a register circuit.
    A. Serial data to serial  
    B. Serial data to parallel  
    C. Parallel data to serial  

    □ Answer - Click Here:  
    B
12. changing in input more than one state is called ________________

A. undefined condition
B. ideal condition
C. reset condition
D. race condition

D